

Abstract of the Disclosure

Common Controller Area Network Interface

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An electronic interface is provided for connecting to various types of networks. The network places a data stream on a bus high signal and a bus low signal. The interface includes a ground synthesizer circuit, a capacitive isolator circuit, and an edge triggering circuit. The ground synthesizer circuit, coupled to the bus high signal and bus low signal, synthesizes a ground from the bus high signal and bus low signal. The capacitive isolator circuit, coupled to the ground synthesizer circuit, generates an isolated bus high signal from the bus high signal. The edge triggering circuit, coupled to the capacitive isolator circuit, regenerates the data stream into a first reconstructed data stream by comparing the isolated bus high signal with the ground. The resulting circuit comprises a digital data receiver that may be part of a transceiver.